

L Number	Hits	Search Text	DB	Time stamp
-	50	("4625310" "5690575" "5293079" "4348759" "4507576" "4800294" "5192881" "5304923" "5336944" "5365167" "5530370" "5604750" "5682352" "5844913" "5951705" "5999013" "6194910" "6202186" "6232759" "6348785" "4523143" "5500603" "5936876" "6043672" "6083848" "4907230" "5353308" "5623500" RE31828 "4504783" "4608669" "4782283" "5049814" "5177440" "5243274" "5258985" "5272434" "5394098" "5524114" "5570034" "5583786" "5610826" "5617531" "5621312" "5680936" "5805611" "5818251" "5878051" "5889936" "5905381") .pn.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 09:40
-	1265	(variable change\$4 flexible shifting adapt\$4 alter\$4 adjust\$4 switch\$4) with (voltage adj generat\$3) and (sense adj amp\$6)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:40
-	110022	reference adj voltage	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 09:47
-	1853	(loop adj back loopback) with (generat\$3 compar\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 09:48
-	81043	DAC D/A adj convert\$3 digital-to-analog adj convert\$3	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 09:55
-	29832	compar\$4 same reference same (analog direct adj current DC) same (volt\$3 power level\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:00

	2300	boundary-scan boundary adj scan	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:02
	1	((variable change\$4 flexible shifting adapt\$4 alter\$4 adjust\$4 switch\$4) with (voltage adj generat\$3) and (sense adj amp\$6)) and (reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3)) and (DAC D/A adj convert\$3 digital-to-analog adj convert\$3 ) and (compar\$4 same reference same (analog direct adj current DC) same (volt\$3 power level\$3)) and (compar\$4 same reference same (analog direct adj current DC) same (volt\$3 power level\$3)) and (boundary-scan boundary adj scan)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:11
	1	((variable change\$4 flexible shifting adapt\$4 alter\$4 adjust\$4 switch\$4) with (voltage adj generat\$3) and (sense adj amp\$6)) and (reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3)) and (DAC D/A adj convert\$3 digital-to-analog adj convert\$3 ) and (compar\$4 same reference same (analog direct adj current DC) same (volt\$3 power level\$3)) and (compar\$4 same reference same (analog direct adj current DC) same (volt\$3 power level\$3))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:11
	1	((variable change\$4 flexible shifting adapt\$4 alter\$4 adjust\$4 switch\$4) with (voltage adj generat\$3) and (sense adj amp\$6)) and (reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3)) and (DAC D/A adj convert\$3 digital-to-analog adj convert\$3 ) and (compar\$4 same reference same (analog direct adj current DC) same (volt\$3 power level\$3))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:12
	1	((variable change\$4 flexible shifting adapt\$4 alter\$4 adjust\$4 switch\$4) with (voltage adj generat\$3) and (sense adj amp\$6)) and (reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3)) and (DAC D/A adj convert\$3 digital-to-analog adj convert\$3 )	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:12
	263109	(voltage adj generat\$3 source) and (sense adj amp\$6 receiver comparator)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:42
	47441	((voltage adj generat\$3 source) and (sense adj amp\$6 receiver comparator)) and (reference adj voltage) ((loop adj back loopback) with (generat\$3 compar\$3))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:42
	57	((voltage adj generat\$3 source) and (sense adj amp\$6 receiver comparator)) and (reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:50
	1352	(324/522,523,527).ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:44
	2360	(714/819,727,736,734,733).ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:45
	518	(341/94).ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:45

-		0	((voltage adj generat\$3 source) and (sense adj amp\$6 receiver comparator)) and (reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3))) and ((324/522,523,527).ccls.) and ((714/819,727,736,734,733).ccls.) and ((341/94).ccls.)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:46
-		0	((voltage adj generat\$3 source) and (sense adj amp\$6 receiver comparator)) and (reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3))) and ((324/522,523,527).ccls.)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:46
-		72	(reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:48
-		7484	((reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3))) and ((324/522,523,527).ccls.) and ((714/819,727,736,734,733).ccls.) nad ((341/94).ccls.)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:48
-		0	((reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3))) and ((324/522,523,527).ccls.) and ((714/819,727,736,734,733).ccls.) and ((341/94).ccls.)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:49
-		1	((voltage adj generat\$3 source) and (sense adj amp\$6 receiver comparator)) and (reference adj voltage) ((loop adj back loopback) with (generat\$3 compar\$3))) and ((324/522,523,527).ccls.) and ((714/819,727,736,734,733).ccls.)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:49
-		1		USPAT	2003/12/18 11:11
-		1		USPAT	2003/12/18 11:12
-		1		USPAT	2003/12/18 11:12
-		1		USPAT	2003/12/18 11:26
-		1		USPAT	2003/12/18 11:32



[Advanced Search](#) [Preferences](#) [Language Tools](#) [Search Tips](#)

DAC loop reference voltage regis

[Google Search](#)

Web · Images · Groups · Directory · News ·

Searched the web for **DAC loop reference voltage register switch**. Results 1 - 50 of about 5,840. Search took 0

### Chipdir - Chips by category

... pll, Phase Locked **Loop**, port, Port, buffer, driver, ff, latch, receive, xmit, xcvr, ... video, Video, adc, audio, crt, **dac**, shift, tv, ... vref, **Voltage reference**, volt, ...  
[www.xs4all.nl/~ganswijk/chipdir/f/](http://www.xs4all.nl/~ganswijk/chipdir/f/) - 33k - [Cached](#) - [Similar pages](#)

### Sponsored Links

#### **Voltage Reference ICs**

Huge selection of low cost, high accuracy, small, low power refs!  
[www.maxim-ic.com](http://www.maxim-ic.com)  
 Interest:

[See your message here...](#)

### LeCroy Research Systems Model MHV100 HV Controller

... Built-in Band Gap **Voltage Reference**; Feedback Error Amp for **Voltage Control Loop**;  
 1.6 MHz ... Two uncommitted op-amps and auxiliary **DAC** (8 bit) output and ADC ...  
[www.lecroy.com/lrs/dsheets/mhv100.htm](http://www.lecroy.com/lrs/dsheets/mhv100.htm) - 5k - [Cached](#) - [Similar pages](#)

### [\*\*\[PDF\]\*\* Dual Current-Output Parallel-Input, 16-/14-Bit \*\*DAC\*\* Preliminary ...](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)  
 ... tests are performed in a closed **loop** system using ... R 2R Figure 5. Equivalent R-2R **DAC** Circuit Note ... The **reference voltage** inputs exhibit a constant nominal input ...  
[www.analog.com/UploadedFiles/Data\\_Sheets/43093437AD5547\\_57\\_prc.pdf](http://www.analog.com/UploadedFiles/Data_Sheets/43093437AD5547_57_prc.pdf) - [Similar pages](#)

### [\*\*\[PDF\]\*\* EVAL-AD5379 Preliminary Technical Data](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)  
 ... in the system to avoid ground **loop** problems ... AD5379 40-Channel **DAC** Parallel Interface 28 pin Header ... Serial Interface 36 pin Connector 3V **Reference** VREF (-) Gain ...  
[www.analog.com/UploadedFiles/Evaluation\\_Boards/Tools/25442813AD5379EB\\_prc.pdf](http://www.analog.com/UploadedFiles/Evaluation_Boards/Tools/25442813AD5379EB_prc.pdf) - [Similar pages](#)  
 [ More results from [www.analog.com](http://www.analog.com) ]

### [\*\*\[PDF\]\*\* MAX555 300Msps, 12-Bit \*\*DAC\*\* with Complementary \*\*Voltage\*\* Outputs](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)  
 ... V IN ) ....0V to +1.25V **Reference** Input Current ... MAX555 300Msps, 12-Bit **DAC** with Complementary **Voltage** ... 3 20 AV OL Open-**Loop** Gain MHz ...  
[www.gaw.ru/doc/Maxim/1127.PDF](http://www.gaw.ru/doc/Maxim/1127.PDF) - [Similar pages](#)

### [\*\*\[PDF\]\*\* A Universal Micro-Sensor Interface Chip with Network ...](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)  
 ... Gain Amp: self-test input 6b **DAC** Capacitive Amp ... capacitive readout circuit; with a 3pF **reference** capacitor and ... the input of a closed-**loop** differential amplifier ...  
[www.egr.msu.edu/~mason/pubs/umsi\\_mwscas2002.pdf](http://www.egr.msu.edu/~mason/pubs/umsi_mwscas2002.pdf) - [Similar pages](#)

### [\*\*\[PDF\]\*\* XRD5412](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)  
 ... 85 °C BLOCK DIAGRAM Figure 1. Block Diagram Shift **Register** - + V REFIN V ... Output 5 AGND Analog Ground 6 V REFIN **Voltage Reference** Input 7 V OUT **DAC** Output 8 ...  
[www.exar.com/products/xrd5412.pdf](http://www.exar.com/products/xrd5412.pdf) - [Similar pages](#)

### [\*\*\[PDF\]\*\* XRD5408/10/12](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)  
 ... is constrained by the output swing of the fixed +2 closed-**loop** gain amplifier. ... The **reference voltage** must be positive because the XRD5408/10/12 **DAC** is non ...  
[www.exar.com/products/xrd5408.pdf](http://www.exar.com/products/xrd5408.pdf) - [Similar pages](#)

[IEEE HOME](#) | [SEARCH IEEE](#) | [SHOP](#) | [WEB ACCOUNT](#) | [CONTACT IEEE](#)[Membership](#) [Publications/Services](#) [Standards](#) [Conferences](#) [Careers/Jobs](#)**IEEE Xplore®**  
RELEASE 1.5Welcome  
United States Patent and Trademark Office[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)**Quick Links**

&gt; See

**Welcome to IEEE Xplore®**

- Home
- What Can I Access?
- Log-out

**Tables of Contents**

- Journals & Magazines
- Conference Proceedings
- Standards

**Search**

- By Author
- Basic
- Advanced

**Member Services**

- Join IEEE
- Establish IEEE Web Account
- Access the IEEE Member Digital Library

 [Print Format](#)[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#)  
[Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#)  
[No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2003 IEEE — All rights reserved